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(71) Applicant (for all designated States except US): ASCOM ENERGY SYSTEMS AG [CH/CH]; Belpstrasse 37, CH-3000 Bern 14 (CH).

(72) Inventor; and

(75) Inventor/Applicant (for US only): JITARU, Ionel, Dan [US/US]; 2378 North Sun Lake Place, Tucson, AZ 85711 (US).

(74) Agents: ROSHARDT, Werner, A. et al.; Keller & Partner Patentanwälte AG, Schmiedenplatz 5, Postfach, CH-3000 Bern 7 (CH).

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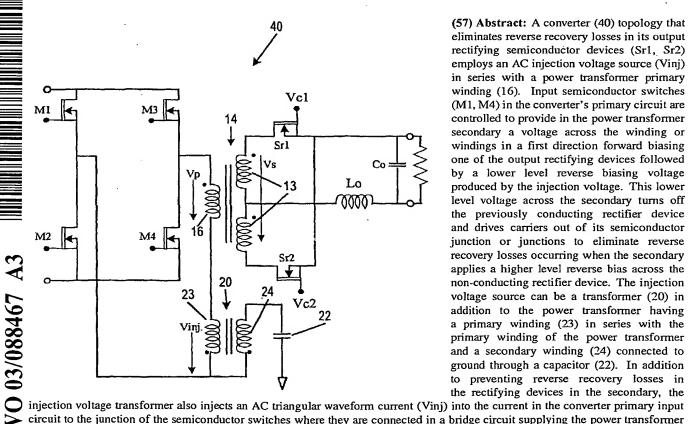
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(54) Title: SOFT SWITCHING CONVERTER USING CURRENT SHAPING



(57) Abstract: A converter (40) topology that eliminates reverse recovery losses in its output rectifying semiconductor devices (Sr1, Sr2) employs an AC injection voltage source (Vinj) in series with a power transformer primary winding (16). Input semiconductor switches (M1, M4) in the converter's primary circuit are controlled to provide in the power transformer secondary a voltage across the winding or windings in a first direction forward biasing one of the output rectifying devices followed by a lower level reverse biasing voltage produced by the injection voltage. This lower level voltage across the secondary turns off the previously conducting rectifier device and drives carriers out of its semiconductor junction or junctions to eliminate reverse

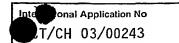
circuit to the junction of the semiconductor switches where they are connected in a bridge circuit supplying the power transformer primary. By this, the injection voltage source assures zero voltage switching of the semiconductor switches even at light loads.

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INTERNATIONAL SEARCH REPORT ional Application No CH 03/00243 A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H02M3/337 H02M3/28 According to International Patent Classification (IPC) or to both national classification and IPC B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) IPC 7 HO2M Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal C. DOCUMENTS CONSIDERED TO BE RELEVANT Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. X DE DONCKER R W ET AL: "A three-phase 1 - 4soft-switched high power density DC/DC converter for high power applications" 1988 IEEE , 2 October 1988 (1988-10-02), pages 796-805, XP010519176 page 1; figure 1 EP 0 430 242 A (SYSTEL DEV & IND LTD) X 5,7,8, 5 June 1991 (1991-06-05) 10-12. 14,16, 26-28, 33-35 abstract column 3, line 30 - line 58 figure 2 Y Further documents are listed in the continuation of box C. Y Patent family members are listed in annex.

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European Patent Office, P.B. 5818 Patentlaan 2 NL – 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016	Marannino, E.

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